

## computer knowledge-computer organization mcq test-paper2

**Which memory unit has lowest access time?**

- ☐ A Cache
- ☐ B Registers
- ☐ C Magnetic Disk
- ☐ D Pen drive

**Answer : B**

**A digital circuit that generates the arithmetic sum of two binary number of any length is**

- ☐ A Binary Adder
- ☐ B Binary Incrementer
- ☐ C Multiplexer
- ☐ D Binary Adder-Subtractor unit

**Answer : A**

**Which of the memory has highest speed?**

- ☐ A main memory
- ☐ B cache memory
- ☐ C internal memory
- ☐ D secondary memory

**Answer : C**

**Virtual memory is**

- ☐ A an extremely large main memory
- ☐ B an extremely large secondary memory
- ☐ C an illusion of an extremely large memory
- ☐ D a type of memory used in super computers

**Answer : C**

**Which of the following is example of secondary memory?**

- ☐ A ROM
- ☐ B RAM
- ☐ C registers
- ☐ D hard disk

**Answer : D**

**Interrupts which are initiated by an instruction are**

- ☐ A internal
- ☐ B software
- ☐ C external
- ☐ D hardware

**Answer : B**

**The slow speed and low cost devices that provide backup storage are called**

- ☐ A auxiliary memory
- ☐ B primary memory
- ☐ C cache memory
- ☐ D internal memory

**Answer : A**

**The branch logic that provides decision making capabilities in the control unit is known as**

- ☐ A controlled transfer
- ☐ B conditional transfer
- ☐ C unconditional transfer
- ☐ D none of above

**Answer : D**

**The maximum amount of information that can be transferred to or from the memory per unit time is called**

- ☐ A Frequency
- ☐ B Bandwidth
- ☐ C Access time
- ☐ D Cycle time

**Answer : B**

**The adder which performs the addition of two binary numbers serially bit by bit starting with LSB is**

- ☐ A Serial Adder
- ☐ B Ripple Carry Adder
- ☐ C Carry Look-ahead Adder
- ☐ D Parallel Adder

**Answer : A**

**A stack is**

- ☐ A an 8-bit register in the microprocessor
- ☐ B a 16-bit register in the microprocessor

**C** a set of memory locations in R/WM reserved for storing information temporarily during the execution of computer

**D** a 16-bit memory address stored in the program counter

**Answer : A**

**Which of the following memory has highest storage capacity?**

**A** Registers

**B** Magnetic disks

**C** SRAM

**D** DRAM

**Answer : B**

**Pipelining strategy is called implement**

**A** instruction execution

**B** instruction prefetch

**C** instruction decoding

**D** instruction manipulation

**Answer : C**

**The special high-speed memory used to increase speed of processing by making current data or program available to the cpu at rapid rate is**

**A** internal memory

**B** cache memory

**C** secondary memory

**D** primary memory

**Answer : B**

**Which of the following is example of internal memory?**

**A** Registers

**B** hard disk

**C** RAM

**D** EEPROM

**Answer : A**

**Which of the following is not the access method related to memory?**

**A** serial access

**B** random access

**C** associative access

☐ D specialization access

**Answer : D**

**CPU does not perform the operation**

- ☐ A data transfer
- ☐ B logic operation
- ☐ C arithmetic operation
- ☐ D all of above

**Answer : B**

**A popular type of EEPROM in which erasing is performed in large blocks rather than bits by bits is \_\_\_\_\_**

- ☐ A Flash Memory
- ☐ B PROM
- ☐ C Magnetic disks
- ☐ D None of the above

**Answer : D**

**A complete microcomputer system consists of**

- ☐ A microprocessor
- ☐ B memory
- ☐ C peripheral equipment
- ☐ D all of above

**Answer : D**

**Which of the following are functional part of associative memory?**

- ☐ A Input register
- ☐ B Mask register
- ☐ C Select register
- ☐ D All the above

**Answer : D**