## computer knowledge-computer organization mcq test-paper6

## This unit performs arithmetic logic operations on data values stoered in registers.

Α	Control unit
В	ALU
С	Register Set

D Microprocessor

## Answer: B

#### Memory unit accessed by content is called



Read only memory

В Programmable Memory

- С Virtual Memory
- D Associative Memory

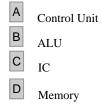
# Answer : D

#### The instruction 'ORG O' is a

- A Machine Instruction
- в Pseudo instruction
- С High level instruction
- D Memory instruction

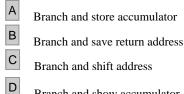
## Answer: B

#### The sequence of operation in CPU is controlled by



#### Answer : A

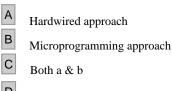
## The BSA instruction is



Branch and show accumulator

## **Answer** : B

Control Units are designed using which of the following approach?



D None of the above

# Answer : C

## MIMD stands for

- A Multiple instruction multiple data
- B Multiple instruction memory data
- C Memory instruction multiple data
- D Multiple information memory data

# Answer : A

## What is the full form of CMBR?

- A Control Memory Buffer Register
- B Condition Memory Buffer Register
- C Control Memory Biased Register
- D Conditional Memory Biased Register

# Answer : A

## The time interval between adjacent bits is called the

A Word-time
 B Bit-time
 C Turn around time
 D Slice time

## Answer : B

## A group of bits that tell the computer to perform a specific operation is known as

- A Instruction code B Micro-operation
- C Accumulator
- D Register

# Answer : A

## The I/O Devics are also known as

- A Framework
- B Peripherals
- C Firmware



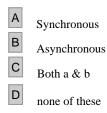
# Answer: B

#### An instruction pipeline can be implemented by means of

Α	LIFO buffer
В	FIFO buffer
С	Stack
D	None of the above

# **Answer** : B

#### The mode of data transfer in which sending and receiving units are enabled with same clock is



# Answer : A

#### Main measurement of CPU performance is/are

A	execution time
В	throughput
С	both of the above
D	none of the above

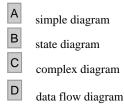
# Answer : C

## The performance of cache memory is frequently measured in terms of a quantity called

Α	Miss ratio
В	Hit ratio
С	Latency ratio
D	Read ratio

# Answer : C

#### The information available in a state table may be represented graphically in a



## Answer: B

#### Full form of MIPS is \_\_\_\_\_

- A Millions of Instructions per second
- в Millions of Issues per second
- С Millions of Indexing per second
- D Millions of Interrupt per second

## Answer : A

#### Full form of MFLOPS is \_\_\_\_\_

- А Millions of Fixed Point Operations Per Second В Millions of Floating Point Operations Per Second
- С Millions of Floating Point Opcodes Per Second
- D None of the above

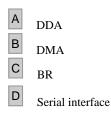
# Answer : B

## Which of the following are parallel computers?

- A pipeline computer
- в array processor

array processor
multiprocessor system
All of the above

Answer: D
An interface that provides I/O transfer of data directly to and form the memory unit and peripheral is termed as



Answer: B