computer knowledge-computer organization mcq test-paper3

The performance of cache memory can be represented as

- A number of hits/total CPU references to the memory
- B number of hits/(number of hits + number of misses)
- C Both A & B
 - number of miss/(number of hits + number of misses)

Answer : C

D

The branch logic that provides decision making capabilities in the control unit is known as

- A controlled transfer
- B conditional transfer
- C unconditional transfer
- D none of the above

Answer : C

The type of mapping used by cache memory is/are

- A associative mappingB direct mapping
- C set-associative mapping
- D All of the above

Answer : D

In Reverse Polish notation, expression A*B+C*D is written as

- A AB*CD*+
- B A*BCD*+
- C AB*CD+*
- D A*B*CD+

Answer : A

The performance of cache memory can be represented as

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- D Only A & B

Answer : D

SIMD represents an organization that _____

A	
tim	e

refers to a	a computer	system	capable	of processing	several	programs	at the	same

B represents organization of single computer containing a control unit, processor unit and a memory unit.

C includes many processing units under the supervision of a common control unit

D None of the above

Answer : C

The type of mapping used by cache memory is/are

- A associative mapping
- B direct mapping
- C set-associative mapping
- D All of the above

Answer : D

Floating point representation is used to store

- A Boolean values
 B real integers
 C whole numbers
 D integers
- D integers

Answer : B

Which of the following cache memory can store two or more words under the same index address?

- A Associative mapping B Direct Mapping
- Direct Mapping
- C Set-Associative mapping
- D All of the above

Answer : C

Which of the following are replacement methods used by cache memory?

- A FIFO algorithm
- B LIFO algorithm
- C Least Recently Used algorithm
- D Both A & C

Answer : D

A

В

In computers, subtraction is generally carried out by

9`s complement

10`s complement



2's complement

Answer : D

Which of the following are correct cache writing method(s)?



write-on policy write-through policy

С write-back policy

D Both B & C

Answer: D

The amount of time required to read a block of data from a disk into memory is composed of seek time, rotational latency, and transfer time. Rotational latency refers to



the time its takes for the platter to make a full rotation

В the time it takes for the read-write head to move into position over the appropriate track

С the time it takes for the platter to rotate the correct sector under the head

D none of the above

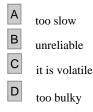
Answer : A

Three C's of misses related to cache memory are

- A Calculative, Capacity, Conflict
- в Compulsory, Capacity, Conflict
- С Calculative, Confirmatory, Conflict
- D Calculative, Confirmatory, Conclusive

Answer : B

What characteristic of RAM memory makes it not suitable for permanent storage?

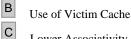


Answer : C

Which of the following is not the correct technique to reduce cache miss rate?



Compiler Optimization

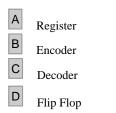


Lower Associativity



Answer : C

The circuit used to store one bit of data is known as



Answer : D

Which of the following is not the type of Cache memory?



- Instruction Cache
- Informative Cache
- С Unified Cache
- D Data Cache

Answer: B

ess is ... V. freeonlinetest.in In virtual memory the logical address is mapped to physical address by



Memory Management Unit

в Memory mapper

С Logical address space

D None of the above

Answer : A

(2FAOC) 16 is equivalent to



- (195 084) 10 в
 - (001011111010 0000 1100) 2
- С Both (A) and (B)
- D None of the above

