

computer knowledge-computer organization mcq test-paper4

Which of the following is ways to configure virtual memory?

- ☐ A Aggregation technique
- ☐ B Paging technique
- ☐ C Segmentation technique
- ☐ D Both b & c

Answer : D

The average time required to reach a storage location in memory and obtain its contents is called the

- ☐ A seek time
- ☐ B turnaround time
- ☐ C access time
- ☐ D transfer time

Answer : C

The non-contiguous memory allocation method in virtual memory is called

- ☐ A Paging
- ☐ B Segmentation
- ☐ C Aggregation
- ☐ D None of the above

Answer : A

Which of the following is not a weighted code?

- ☐ A Decimal Number system
- ☐ B Excess 3-code
- ☐ C Alphanumeric Codes
- ☐ D Error Detecting Codes

Answer : B

In paging, operating system maintains data structure called _____ to map logical to physical address

- ☐ A data table
- ☐ B page table
- ☐ C mapper table
- ☐ D hash table

Answer : B

The idea of cache memory is based

- ☐ A on the property of locality of reference
- ☐ B on the heuristic 90-10 rule
- ☐ C on the fact that references generally tend to cluster
- ☐ D all of the above

Answer : A

Paging may suffer from problem known as

- ☐ A memory break
- ☐ B data leakage
- ☐ C Page break
- ☐ D index break

Answer : B

Which of the following is lowest in memory hierarchy?

- ☐ A Cache memory
- ☐ B Secondary memory
- ☐ C Registers
- ☐ D RAM

Answer : B

The addressing mode used in an instruction of the form ADD X Y, is

- ☐ A Absolute
- ☐ B indirect
- ☐ C index
- ☐ D None of the above

Answer : C

Which of the following replacement algorithm can be used in page- break.

- ☐ A FIFO
- ☐ B LRU
- ☐ C optimal
- ☐ D All of the above

Answer : D

If memory access takes 20 ns with cache and 110 ns with out it, then the ratio (cache uses a 10 ns memory) is

- ☐ A 85%
- ☐ B 90%
- ☐ C 95%

☐ D 100%

Answer : B

"Select a page for replacement, that has been in the main memory for longest period of time." Which replacement algorithm follows this rule?

- ☐ A FIFO
- ☐ B LRU
- ☐ C optimal
- ☐ D All of the above

Answer : A

In a memory-mapped I/O system, which of the following will not be there?

- ☐ A LDA
- ☐ B IN
- ☐ C ADD
- ☐ D OUT

Answer : B

The memory management scheme that supports the user view of memory

- ☐ A Paging
- ☐ B Segmentation
- ☐ C Aggregation
- ☐ D Abstraction

Answer : B

Von Neumann architecture is

- ☐ A SISD
- ☐ B SIMD
- ☐ C MIMD
- ☐ D MISD

Answer : A

Translation look Aside Buffer is used to hold

- ☐ A least recently used page table entries
- ☐ B most recently used page table entries
- ☐ C both of the above
- ☐ D None of the above

Answer : C

The circuit used to store one bit of data is known as

- ☐ A Encoder
- ☐ B OR gate
- ☐ C Flip Flop
- ☐ D Decoder

Answer : C

Cache memory acts between

- ☐ A CPU and RAM
- ☐ B RAM and ROM
- ☐ C CPU and Hard Disk
- ☐ D None of the above

Answer : A

Before finalizing the instruction set for a computer, computer architects need to consider

- ☐ A flexibility to programmer
- ☐ B number of addressing modes
- ☐ C system performance
- ☐ D All of the above

Answer : D

Write Through technique is used in which memory for updating the data

- ☐ A Virtual memory
- ☐ B Main memory
- ☐ C Auxiliary memory
- ☐ D Cache memory

Answer : D