

computer knowledge-computer organization mcq test-paper5

Generally Dynamic RAM is used as main memory in a computer system as it

- ☐ A Consumes less power
- ☐ B has higher speed
- ☐ C has lower cell density
- ☐ D needs refreshing circuitary

Answer : B

Which of the following is not correct CPU organisation with specific instruction

- ☐ A Single Accumulator Organization
- ☐ B multiple register
- ☐ C General register organization
- ☐ D Stack organization

Answer : B

In signed-magnitude binary division, if the dividend is (11100)₂ and divisor is (10011)₂ then the result is

- ☐ A (00100)₂
- ☐ B (0100)₂
- ☐ C (11001)₂
- ☐ D (01100)₂

Answer : B

Stack based CPU organisation uses this access method?

- ☐ A LIFO
- ☐ B FIFO
- ☐ C Binary tree
- ☐ D Linked list

Answer : A

Arithmetic operations are usually represented in

- ☐ A infix notation
- ☐ B Polish notation
- ☐ C reverse polish notation
- ☐ D binary notation

Answer : A

Logic X-OR operation of (4ACO) H & (B53F) H results

- ☐ A AACB
- ☐ B 0000
- ☐ C FFFF
- ☐ D ABCD

Answer : C

A subroutine program segment for carrying out repeatedly needed tasks is

- ☐ A booting
- ☐ B subroutines
- ☐ C Stack operator
- ☐ D None of the above

Answer : B

When CPU is executing a Program that is part of the Operating System, it is said to be in

- ☐ A Interrupt mode
- ☐ B System mode
- ☐ C Half mode
- ☐ D Simplex mode

Answer : B

An n-bit microprocessor has ___

- ☐ A An n-bit microprocessor has
- ☐ B n-bit instruction register
- ☐ C n-bit address register
- ☐ D n-bit ALU

Answer : B

The different ways in which the location of an operand is specified in an instruction is referred as

- ☐ A pointer modes
- ☐ B execution modes
- ☐ C Addressing modes
- ☐ D Instruction modes

Answer : C

Cache memory works on the principle of<

- ☐ A Locality of data
- ☐ B Locality of memory
- ☐ C Locality of reference

☐ D Locality of reference & memory

Answer : C

The processing required for a single instruction is called

- ☐ A instruction cycle
- ☐ B processing cycle
- ☐ C unary cycle
- ☐ D data cycle

Answer : A

The main memory in a Personal Computer (PC) is made of

- ☐ A cache memory
- ☐ B static RAM
- ☐ C Dynamic Ram
- ☐ D both (A) and (B)

Answer : D

What is the full form of CISC?

- ☐ A Complex Instruction Set Computer
- ☐ B Compound Information Set Computer
- ☐ C Compound Instruction Set Computer
- ☐ D Complex Information Set Computer

Answer : A

PSW is saved in stack when there is a

- ☐ A interrupt recognised
- ☐ B Execution of RST instruction
- ☐ C Execution of CALL instruction
- ☐ D None of these

Answer : A

What is the full form of RISC?

- ☐ A Regular Instruction Set Computer
- ☐ B Regular Information Set Computer
- ☐ C Reduced Instruction Set Computer
- ☐ D Reduced Information Set Computer

Answer : C

Which of the following statement is correct?

(i) RISC system are more popular due to their performance level as compared to CISC

(ii) VAX 11/780 is an example of RISC

- ☐ A only (i)
- ☐ B only (ii)
- ☐ C both (i) & (ii)
- ☐ D None of the above

Answer : A

The circuit converting binary data in to decimal is

- ☐ A Encoder
- ☐ B Multiplexer
- ☐ C Decoder
- ☐ D Code converter

Answer : D

A three input NOR gate gives logic high output only when

- ☐ A one input is high
- ☐ B one input is low
- ☐ C two input are low
- ☐ D all input are high

Answer : D

Which of the following is an example of RISC?

- ☐ A POWerPC
- ☐ B VAX 11/780
- ☐ C Intels Pentium
- ☐ D All of the above

Answer : A