

computer knowledge-microprocessor mcq online test-paper1

Which is the parts of microprocessor?

- ☐ A ALU
- ☐ B Register section
- ☐ C Control Unit
- ☐ D All of the above

Answer : D

Microprocessor is a unit that controls

- ☐ A Microcomputer
- ☐ B Laptop
- ☐ C Input-output devices
- ☐ D All of the above

Answer : A

ALU stands for?

- ☐ A Arithmetic logical unit
- ☐ B Addition logic unit
- ☐ C Adders logic unit
- ☐ D None of the above

Answer : A

_____ is the store by register?

- ☐ A Data
- ☐ B Memory
- ☐ C Current
- ☐ D None of the above

Answer : A

Which of the following technology was used by Intel to design its first 8-bit microprocessor?

- ☐ A NMOS
- ☐ B HMOS
- ☐ C PMOS
- ☐ D All of the above

Answer : C

BCD stands for:

- ☐ A Binary coded decimal
- ☐ B Binary coded decoded
- ☐ C Both A & B
- ☐ D None of the above

Answer : A

What does the microprocessor do with instructions from the memory first?

- ☐ A Fetch
- ☐ B Decode
- ☐ C Execute
- ☐ D None of the above

Answer : A

Which is used to store critical pieces of data during subroutines and interrupts:

- ☐ A Stack
- ☐ B Queue
- ☐ C Accumulator
- ☐ D Data register

Answer : A

Which of the following statement is related to the register array?

- ☐ A It consist of registers like B, C, D, E, H, L
- ☐ B It consists of an accumulator
- ☐ C Both A & B
- ☐ D None of the above

Answer : C

It determines the number of operations per second?

- ☐ A Bandwidth
- ☐ B Clock Speed
- ☐ C Operations Speed
- ☐ D Word Length

Answer : B

Which of the following sequence that a microprocessor follows?

- ☐ A Fetch, decode, execute
- ☐ B Fetch, execute, decode
- ☐ C Decode, fetch, execute

☐ D All of the above

Answer : A

The data in the stack is called?

☐ A Pushing data

☐ B Pushed

☐ C Pulling

☐ D None of the above

Answer : A

A microprocessor can be classified into?

☐ A 2

☐ B 3

☐ C 4

☐ D 5

Answer : B

In 8-bit microprocessor, how many opcodes are present?

☐ A 246

☐ B 256

☐ C 248

☐ D 265

Answer : B

Initially the microprocessors stores instructions in _____ order in a memory

☐ A Sequential manner

☐ B Concurrent manner

☐ C Both A & B

☐ D None of the above

Answer : A

Which of the following is not true about the address bus?

☐ A It consists of control PIN 21 to 28

☐ B It is a bidirectional bus

☐ C It is 16 bits in length

☐ D Lower address bus lines (AD0 – AD7) are called “Line number”

Answer : B

A single instruction processes number of bits in a processor it is called as

- ☐ A Instruction processor
- ☐ B Bandwidth
- ☐ C Clock speed
- ☐ D Frequency

Answer : B

Word length depends on

- ☐ A Internal bus width
- ☐ B ALU
- ☐ C Registers
- ☐ D All of the above

Answer : D

Which of the following are the data types used in a microprocessor?

- ☐ A Binary
- ☐ B ASCII
- ☐ C BCD
- ☐ D All of the above

Answer : D

Which of the following is not a CISC processors?

- ☐ A IBM 370/168
- ☐ B Dell 5435
- ☐ C Intel 80486
- ☐ D VAX 11/780

Answer : B